



1503.68591

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Makoto Nakanishi

Serial No.: 10/692,533

Conf. No.: 9889

Filed: October 24, 2003

For: PARALLEL PROCESSING
METHOD FOR INVERSE
MATRIX FOR SHARED
MEMORY TYPE SCALAR
PARALLEL COMPUTER

Art Unit: 2193

Examiner: Mai, Tan V.

) I hereby certify that this paper is being deposited with the
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) February 25, 2008

) Date

) F-CLASS.WCM

) Appr. February 20, 1998

Jan K. Jon
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Attorney for Applicant(s)

RESPONSE C

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Advisory Action dated January 11, 2008 and the Final
Office Action mailed September 24, 2007, please consider the following Remarks: